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Towards Fail-safe Air Traffic Control - Flightglobal.com

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tragedies or failures that shortcomings in either or both are exposed.

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A fail-safe complex CMOS gate is a safe gate. 2nd NASA SERC Symposium on VLSI Design 1990 2.3.5
Definition 4 A Basic Gate is a safe gate with the static latch at the output removed; ie., it is the complex CMOS gate from which the safe gate is derived.

A Fail-Safe CMOS Logic Gate - NASA

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